

PATENT APPLICATION
Docket No. 8750-057
Client No. SPX200303-009US

Applicant: Sang-Eun Lee and Yun-Heub Song
Filing Date: February 5, 2004 Art Unit: not yet assigned
Serial No.: not yet assigned Examiner: not yet assigned
For: METHOD OF MANUFACTURING A SEMICONDUCTOR INTEGRATED CIRCUIT
USING A SELECTIVE DISPOSABLE SPACER TECHNIQUE AND SEMICONDUCTOR
INTEGRATED CIRCUIT MANUFACTURED THEREBY

**INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)**

U.S. PATENT DOCUMENTS

<u>Exam Init</u>	<u>Ref</u>	<u>Document Number</u>	<u>Issue Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub Class</u>
_____	_____	6,043,537	3/28/2000	Jun, et al.		

Examiner: _____

Date Considered: _____